

FIG. I

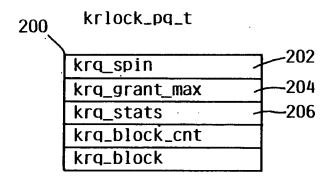
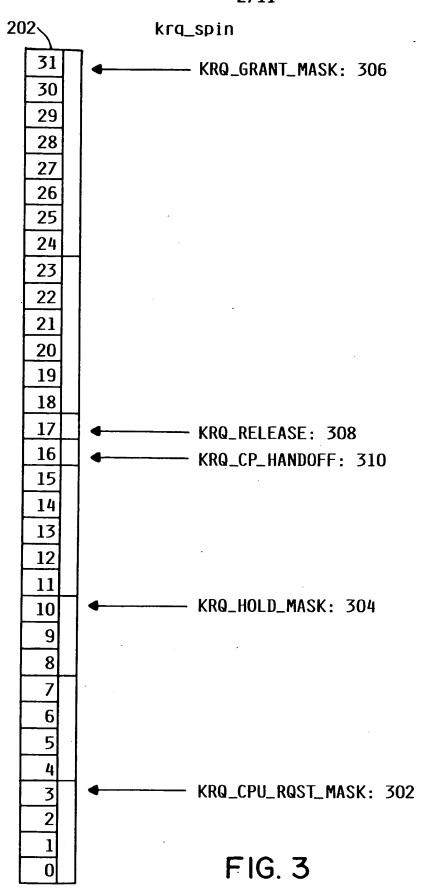
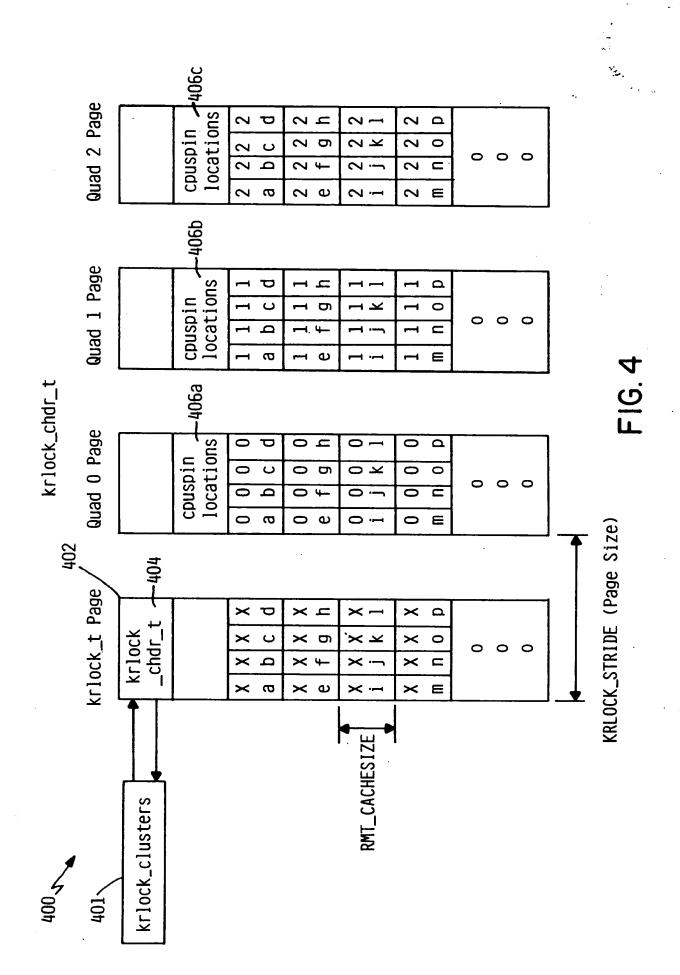


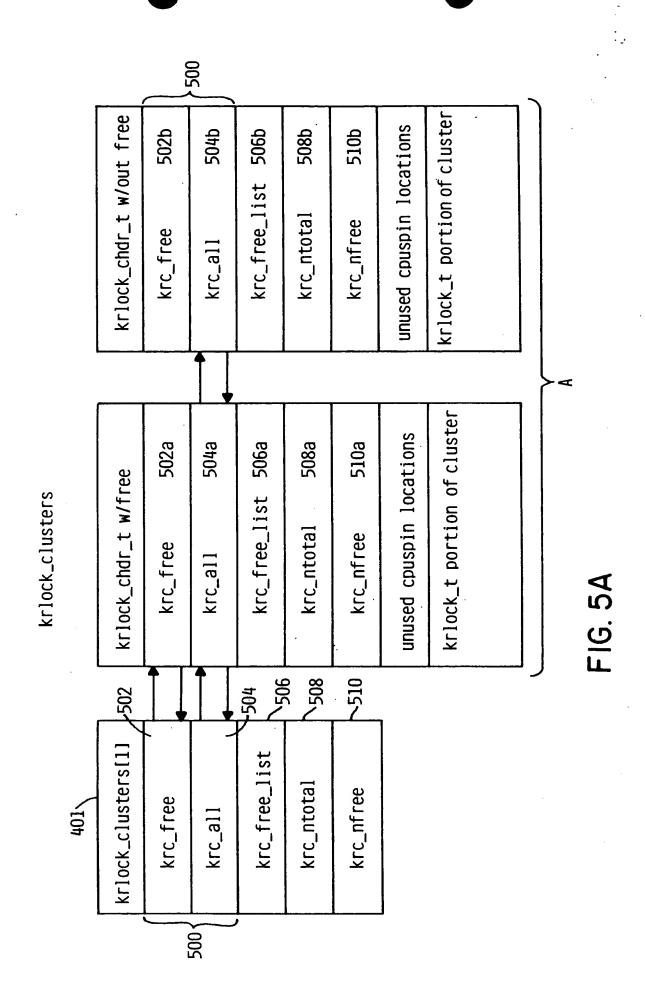
FIG. 2

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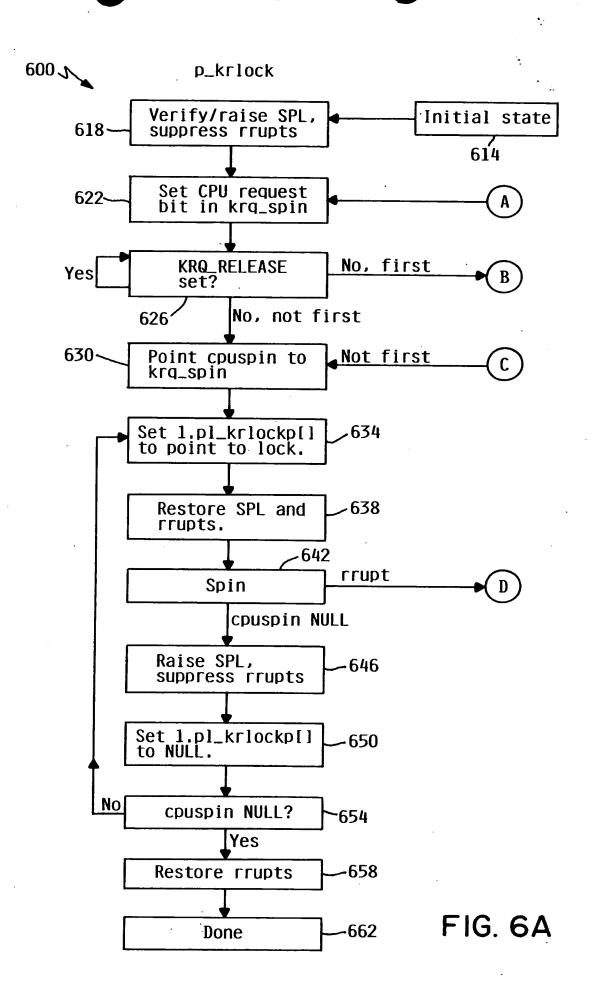
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	A
unused krlock_chdr_t	unused krlock_chdr_t
cpuspin locations	cpuspin locations
Quad O portion of cluster	Quad O portion of cluster
unused krlock_chdr_t	unused krlock_chdr_t
cpuspin locations	cpuspin locations
Quad 1 portion of cluster	Quad 1 portion of cluster
unused krlock_chdr_t	unused krlock_chdr_t
cpuspin locations	cpuspin locations
Quad 2 portion of cluster	Quad 2 portion of cluster

FIG. 5B



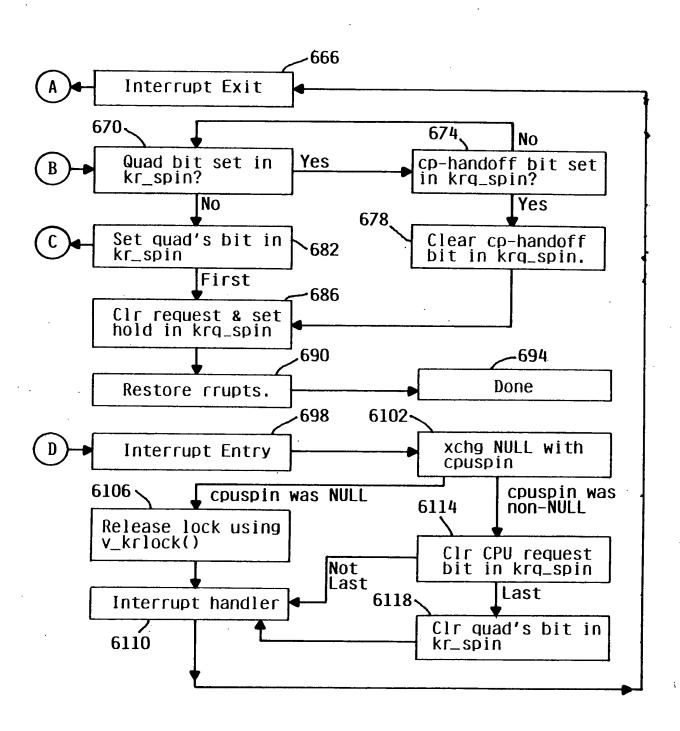


FIG. 6B

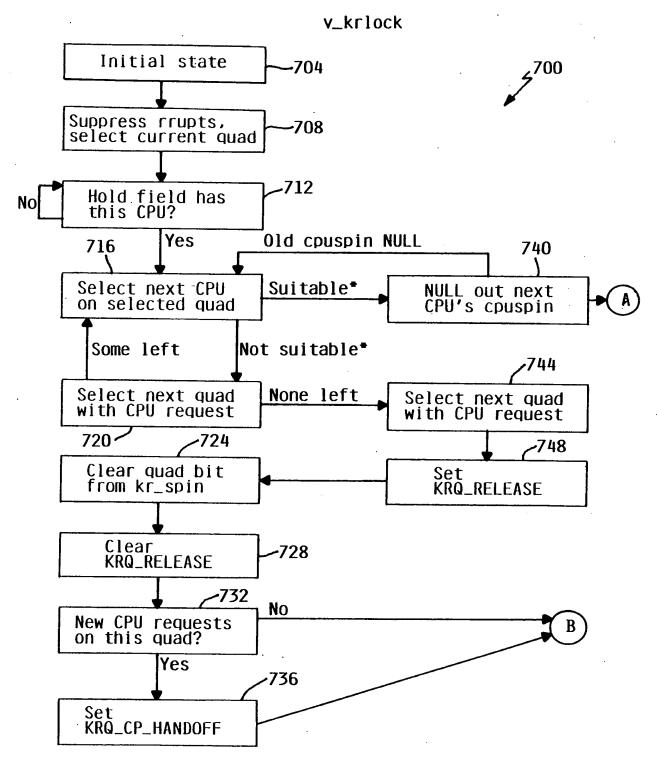


FIG. 7A

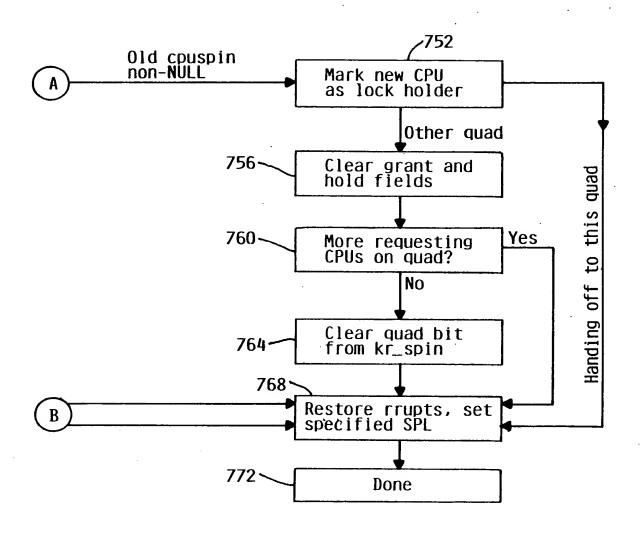


FIG. 7B



cp_krlock

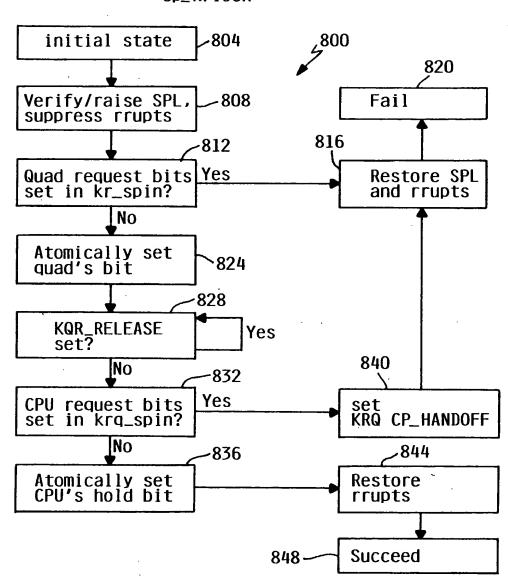


FIG. 8

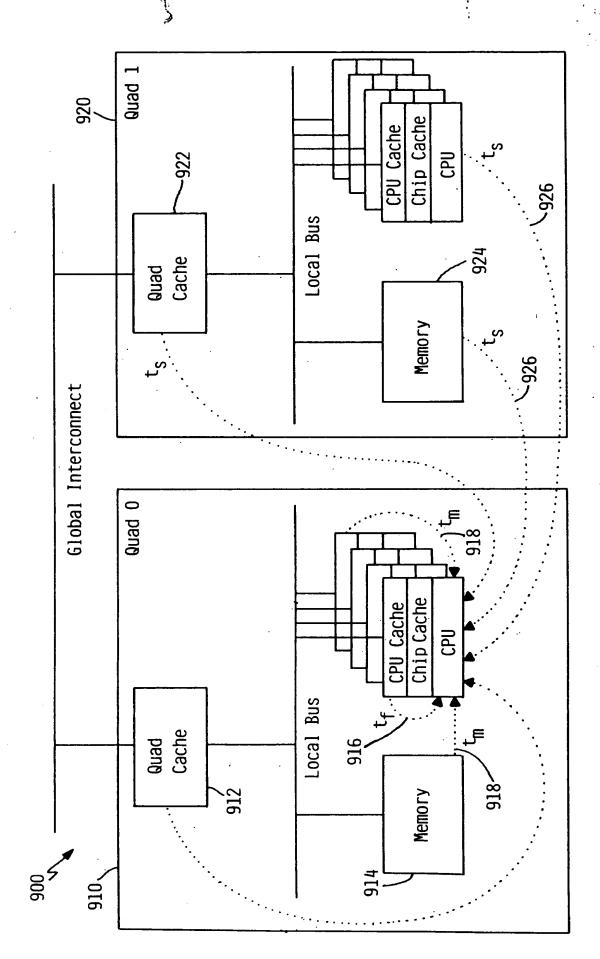


FIG. 9 (PRIOR ART)